PTO/SB/084(10-01)
Approved for use through 1031/2002 (AB 65-1003)
US Petert & Trubement Office US, DEPARTMENT OF COMMERCE
ONLY Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid CMB control number.

Substitute for form 1449A/PTO	Complete if Known		
INFORMATION DISCLOSURE	<b>Application Number</b>	10/643,758	
STATEMENT BY APPLICAND	Filing Date	August 18, 2003	
/ /	First Named Inventor	Sheets, Kitrick	
	Group Art Unit	2186	
FEB	Examiner Name	Tsai, Sheng	
Sheet 1 of 3	Attorney Docket No: 1	376.729US1	

US PATENT DOCUMENTS					
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate	
5jx	US-2002/0169938A1	11/14/2002	Scott, S. L., et al.	.12/14/2001	
at	US-2002/017219/9A1	11/21/2002	Scott, S. L., et al.	12/14/2001	
s)t	US-2004/0044872A1	03/04/2004	Scott, S. L.	09/04/2002	
2)X	US-2004/0162949A1	08/19/2004	Scott, S. L., et al.	02/18/2003	
7t	US-2005/0044128A1	02/24/2005	Scott, S. L., et al.	08/18/2003	
50 t	US-4,888,679	12/19/1989	Fossum, Tryggve, et al.	01/11/1988	
50X	US-5,430,850	07/04/1995	Papadopoulos, G. M., et al.	07/22/1991	
5)t	US-5,446,915	08/29/1995	Pierce, P. R.	05/25/1993	
9t	US-5,560,029	09/24/1996	Papadopoulos, G. M., et al.	05/31/1994	
Soft	US-5,649,141	07/15/1997	Yamazaki, T.	06/30/1995	
	US-5,860,146	01/12/1999	Vishin, S., et al.	06/25/1996	
sit	US-5,897,664	04/27/1999	Nesheim, W. A., et al.	07/01/1996	
90t	US-6,003,123	12/14/1999	Carter, N. P., et al.	02/10/1998	
Spt	US-6,101,590	08/08/2000	Hansen, C.	10/10/1995	
9)t	US-6,308,250	10/23/2001	Klausler, P. M.	06/23/1998	
\$t 5t	US-6,490,671	12/03/2002	Frank, R. L., et al.	05/28/1999	
l ~ a	US-6,684,305	01/27/2004	Deneau, T. M.	04/24/2001	
5t	US-6,782,468	08/24/2004	Nakazato, S.	12/13/1999	
at	US-6,922,766	07/26/2005	Scott, S. L.	09/04/2002	
50 A	US-R,E28,577	10/21/1975	Schmidt, William G.	11/21/1973	
<u> </u>			<u></u>		

Examiner Sheng Ju Juan

PTO/SB/08A(10-01)
Approved for use through 10/31/2002, QNB 651-0031
US Patent 6 Tretement Office: U.S. CEPARTMENT OF COMMERCE
and to a collection of information unless it contiens a valid QNB control number.

Substitute for form 1449APTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)  Sheet 2 of 3	Complete if Known			
	<b>Application Number</b>	10/643,758		
	Filing Date	August 18, 2003		
	First Named Inventor	Sheets, Kitrick		
	Group Art Unit	2186		
	Examiner Name	Tsai, Sheng		
	Attorney Docket No: 1	376.729US1		

FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T²

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No '	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
sit		ABTS, D, "So Many States, So Little Time: Verifying Memory Coherence in the Cray X1", Parallel and Distributed Processing Symposium, (April 22, 2003),11-20	
5/X		CHEN, Y., et al., "UTLB: A Mechanism for Address Translation on Network Interfaces", Proceedings of the Eighth International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS), (1998),193-204	
sit		FAANES, G. J., et al., "DECOUPLED VECTOR ARCHITECTURE", <u>US Patent Application Ser. No. 10/643,586</u> , filed August 18, 2003 (1376.699US1), 47 Pages	
Sat		GHARACHORLOO, KOUROSH, "Two Techniques to Enhance the Performance of Memory Consistency Models", <u>Proceedings of the International Conference on Parallel Processing</u> , (1991),1-10	
cat		HENNESSY, J. L., Computer Architecture A Quantitative Approach, Second Edition, Morgan Kaufman Publishers, Inc., (1996),39-41	
Sat		KONTOTHANASSIS, L., et al., "VM-based shared memory on low-latency, remote-memory-access networks", Proceedings of the ACM ISCA '97, (1997),157-169	
Sal		SCOTT, S. L., "DECOUPLED STORE ADDRESS AND DATA IN A MULTIPROCESSOR SYSTEM", <u>US Patent Application Ser. 10/643,742, filed August 18, 2003 (1376.697US1)</u> , 27 Pages	
st.		SCOTT, S. L., "LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER", <u>US Patent Application Ser. No. 10/643,585, filed August 18, 2003 (1376.700US1)</u> , 17 Pages	
St.		SCOTT, S. L., et al., "MULTISTREAM PROCESSING MEMORY-AND BARRIER-SYNCHRONIZATION METHOD AND APPARATUS", <u>US Patent Application Ser. No. 10/643,741, filed August 18, 2003 (1376.733US1),</u> 143 pages	

	0.1	^	9.		2/20	11
EXAMINER	Sheng	-Ju	Lai	DATE CONSIDERED	02/25/	2006
	200	7,000				

PTO'SB/08A(10-01)
Approved for use through 10/31/2002, OMB 651-0031
US Pesent & Traisment Office U.S. DEPARTMENT OF COMMERCE
on of information unless il contains a varid OMB control number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)  Sheet 3 of 3	Complete if Known			
	<b>Application Number</b>	10/643,758		
	Filing Date	August 18, 2003		
	First Named Inventor	Sheets, Kitrick		
	Group Art Unit	2186		
	Examiner Name	Tsai, Sheng		
	Attorney Docket No: 1	1376.729US1		

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
< <del>+</del>		SCOTT, S. L., et al., "RELAXED MEMORY CONSISTENCY MODEL", <u>US Patent Application Ser. No. 10/643,754</u> , filed August 18, 2003 (1376.724US1), 144 Pages	
- A-		SCOTT, S., "Synchronization and Communication in the T3E Multiprocessor", ASPLOS, Volume II, (1996),pp. 26-36	
sot		SHEETS, K., et al., "REMOTE-TRANSLATION MECHANISM FOR A MULTINODE SYSTEM", <u>US Patent Application Ser. No. 10/643758, filed August 18, 2003 (1376.729US1)</u> , 42 Pages	
sat		WOOD, D. A., et al., "An in-cache address translation mechanism", <u>Proceedings of the 13th annual international symposium on Computer architecture</u> , (1986),358-365	